

**PATENTS****IN THE UNITED STATES PATENT & TRADEMARK OFFICE****VIA HAND CARRY**

This paper and the papers referred to herein as attached  
have been received in the U.S. Patent & Trademark Office  
on the date stamped below:

**COMBINED STATEMENT UNDER 37 C.F.R. § 3.73(b)-ESTABLISHING RIGHT OF  
ASSIGNEE TO TAKE ACTION  
and  
POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST (REVOCATION OF  
PRIOR POWERS)**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Sun Microsystems, Inc., a corporation organized under the laws of Delaware states that it is the assignee of entire right, title, and interest; in the patents and patent applications listed on the attached APPENDIX A, by virtue of an Assignment from the inventor(s) of each of the patents and patent applications listed on the attached APPENDIX A. Date of Recordal, and Reel and Frame number of Recordal by the United States Patent & Trademark Office identified on the attached APPENDIX A.

Sun Microsystems, Inc., a Delaware Corporation, having principle place of business at 901 San Antonio Road, Palo Alto, California, 94303, as assignee of record of the entire interest of the above identified patent (per above statement under 37 CFR §3.73(b)), hereby states that all powers of attorney previously given are hereby revoked.

Further, the following attorneys and/or agents are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith:

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provided that if any one of said attorneys ceases being affiliated with the law firm of MARTINE & PENILLA, LLP as partner or employee, such attorney's appointment as attorney and all powers derived therefrom shall terminate on the date such attorney ceases being so affiliated; and:

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Marilyn E. Glaubenskleo 12/13/02  
(Signature of authorized person) (date)

Marilyn E. Glaubenskleo  
(type or print name of authorized person)  
Assistant General Counsel, having delegated Power  
to sign on Behalf of Sun Microsystems, Inc.

Title of authorized person

New Altys: Martine Penilla

## 3.73(b) POA Transfer

Sun Microsystems, Inc.

09/321,479	5/27/1999	6,269,470	010166/0896	SUNMP271/2825	P3815/MG	EFFICIENT ROUTING OF CONDUCTORS BETWEEN DATAPATHS	7/31/2001	Issued
09/488,915	1/19/2000	6,510,545	010521/0032	SUNMP283/2825	P4415/MG	AUTOMATED SHIELDING ALGORITHM FOR DYNAMIC CIRCUITS	1/21/2003	Issued
09/938,120	8/22/2001		012118/0899	SUNMP306/2825	P5490/MG	METHOD AND SYSTEM FOR PROVIDING A NETLIST DRIVEN INTEGRATED ROUTER WITH A NON-NETLIST DRIVEN ENVIRONMENT		Pending
09/320,240	5/26/1999		010168/0628	SUNMP266/2825	P3458/MG	PERIPHERAL CORE BUMPS FOR INTEGRATED CIRCUITS		Pending
09/320,241	5/26/1999	6,252,760	010165/0544	SUNMP265/2831	P5457/MG	DISCRETE SILICON CAPACITOR	6/26/2001	Issued
09/996,339	3/21/2002		012649/0639	SUNMP309/2835	P6581/MG	FULLY INTEGRATED COMPUTER RACKING SYSTEM		Allowed
09/703,282	10/31/2000		011638/0899	SUNMP302/3743	P5363/MG	HEATSINK WITH ACTIVE LIQUID BASE		Pending
09/303,103	4/29/1999	6,397,158	010996/0935	SUNMP263/2857	P3223/MG	METHOD FOR DETERMINING CAPACITANCE VALUES FOR QUIETING NOISE POWER CONDUCTORS	5/28/2002	Issued
09/625,522	7/26/2000		010973/0398	SUNMP291/2857	P4659/MG	MULTIPLEXER SELECT LINE EXCLUSIVITY CHECK METHOD AND APPARATUS		Allowed
10/082,431	2/22/2002		012842/0519	SUNMP308/2858	P6557/MG	INTEGRATED ELECTROMIGRATION LENGTH EFFECT TESTING METHOD AND APPARATUS		Pending
09/703,282	10/31/2000					HEATSINK WITH ACTIVE LIQUID BASE		Pending

APPENDIX A  
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